	Search Text
1	fpga or field near5 (programmable gate array)
2	(fpga or field near5 (programmable gate array)) and (program\$6 configur\$6) same (detect\$4 correct\$4 fix\$4 debug\$4 diagnos\$6 verif\$7 test\$4)
3	((fpga or field near5 (programmable gate array)) and (program\$6 configur\$6) same (detect\$4 correct\$4 fix\$4 debug\$4 diagnos\$6 verif\$7 test\$4)) and (computer workstation or (test\$4 degug\$4) near3 system)
4	normal near3 (mode operartion run\$4) and (single near3 (mode operartion run\$4))
5	(((fpga or field near5 (programmable gate array)) and (program\$6 configur\$6) same (detect\$4 correct\$4 fix\$4 debug\$4 diagnos\$6 verif\$7 test\$4)) and (computer workstation or (test\$4 degug\$4) near3 system)) and (normal near3 (mode operartion run\$4) and (single near3 (mode operartion run\$4)))
6	((((fpga or field near5 (programmable gate array)) and (program\$6 configur\$6) same (detect\$4 correct\$4 fix\$4 debug\$4 diagnos\$6 verif\$7 test\$4)) and (computer workstation or (test\$4 degug\$4) near3 system)) and (normal near3 (mode operartion run\$4) and (single near3 (mode operartion run\$4)))) and scan\$4
7	(((((fpga or field near5 (programmable gate array)) and (program\$6 configur\$6) same (detect\$4 correct\$4 fix\$4 debug\$4 diagnos\$6 verif\$7 test\$4)) and (computer workstation or (test\$4 degug\$4) near3 system)) and (normal near3 (mode operartion run\$4) and (single near3 (mode operartion run\$4)))) and scan\$4) and register
8	((((((fpga or field near5 (programmable gate array)) and (program\$6 configur\$6) same (detect\$4 correct\$4 fix\$4 debug\$4 diagnos\$6 verif\$7 test\$4)) and (computer workstation or (test\$4 degug\$4) near3 system)) and (normal near3 (mode operartion run\$4) and (single near3 (mode operartion run\$4)))) and scan\$4) and register) and (realtime or real adj time or dynamic\$4 or host or designer user enginer)
9	((((((((((((((((((((((((((((((((((((((

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10	normal near3 (mode operation run\$4) and (single near3 (mode operation run\$4))
11	(((fpga or field near5 (programmable gate array)) and (program\$6 configur\$6) same (detect\$4 correct\$4 fix\$4 debug\$4 diagnos\$6 verif\$7 test\$4)) and (computer workstation or (test\$4 degug\$4) near3 system)) and (normal near3 (mode operation run\$4) and (single near3 (mode operation run\$4)))
12	((((fpga or field near5 (programmable gate array)) and (program\$6 configur\$6) same (detect\$4 correct\$4 fix\$4 debug\$4 diagnos\$6 verif\$7 test\$4)) and (computer workstation or (test\$4 degug\$4) near3 system)) and (normal near3 (mode operation run\$4) and (single near3 (mode operation run\$4)))) and scan\$4
13	(((((fpga or field near5 (programmable gate array)) and (program\$6 configur\$6) same (detect\$4 correct\$4 fix\$4 debug\$4 diagnos\$6 verif\$7 test\$4)) and (computer workstation or (test\$4 degug\$4) near3 system)) and (normal near3 (mode operation run\$4) and (single near3 (mode operation run\$4)))) and scan\$4) and register
14	((((((fpga or field near5 (programmable gate array)) and (program\$6 configur\$6) same (detect\$4 correct\$4 fix\$4 debug\$4 diagnos\$6 verif\$7 test\$4)) and (computer workstation or (test\$4 degug\$4) near3 system)) and (normal near3 (mode operation run\$4) and (single near3 (mode operation run\$4)))) and scan\$4) and register) and (realtime or real adj time or dynamic\$4 or host or designer user enginer)
15	((((((((((((((((((((((((((((((((((((((
16	((((((((((((((((((((((((((((((((((((((

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17	((((((((((((((((((((((((((((((((((((((
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21	
22	(fpga or field near5 (programmable gate array)) same (detect\$4 correct\$4 fix\$4 debug\$4 bug\$4 diagnos\$6 verif\$7 test\$4) and (computer workstation or (test\$4 debug\$4) near4 system) and normal near3 (mode operation run\$4) and single near3 (mode operation run\$4) and register and (realtime or real adj time or dynamic\$4 or host or designer user enginer) and (714/\$.ccls. or 716/\$.ccls. or 326/\$.ccls.)
23	((fpga or field near5 (programmable gate array)) same (detect\$4 correct\$4 fix\$4 debug\$4 bug\$4 diagnos\$6 verif\$7 test\$4) and (computer workstation or (test\$4 debug\$4) near4 system) and normal near3 (mode operation run\$4) and single near3 (mode operation run\$4) and register and (realtime or real adj time or dynamic\$4 or host or designer user enginer) and (714/\$.ccls. or 716/\$.ccls. or 326/\$.ccls.)) and (protocol and (soc or system near3 chip))
24	((fpga or field near5 (programmable gate array)) same (detect\$4 correct\$4 fix\$4 debug\$4 bug\$4 diagnos\$6 verif\$7 test\$4) and (computer workstation or (test\$4 debug\$4) near4 system) and normal near3 (mode operation run\$4) and single near3 (mode operation run\$4) and register and (realtime or real adj time or dynamic\$4 or host or designer user enginer) and (714/\$.ccls. or 716/\$.ccls. or 326/\$.ccls.)) and (protocol and (soc or system near3 chip or embed\$5))

	Search Text
1	714/725.ccls.
2	714/726.ccls.
3	714/727.ccls.

	Search Text
1	716/1.ccls.
2	716/4.ccls.
3	716/5.ccls.
4	716/16.ccls.
5	716/17.ccls.
6	716/18.ccls.